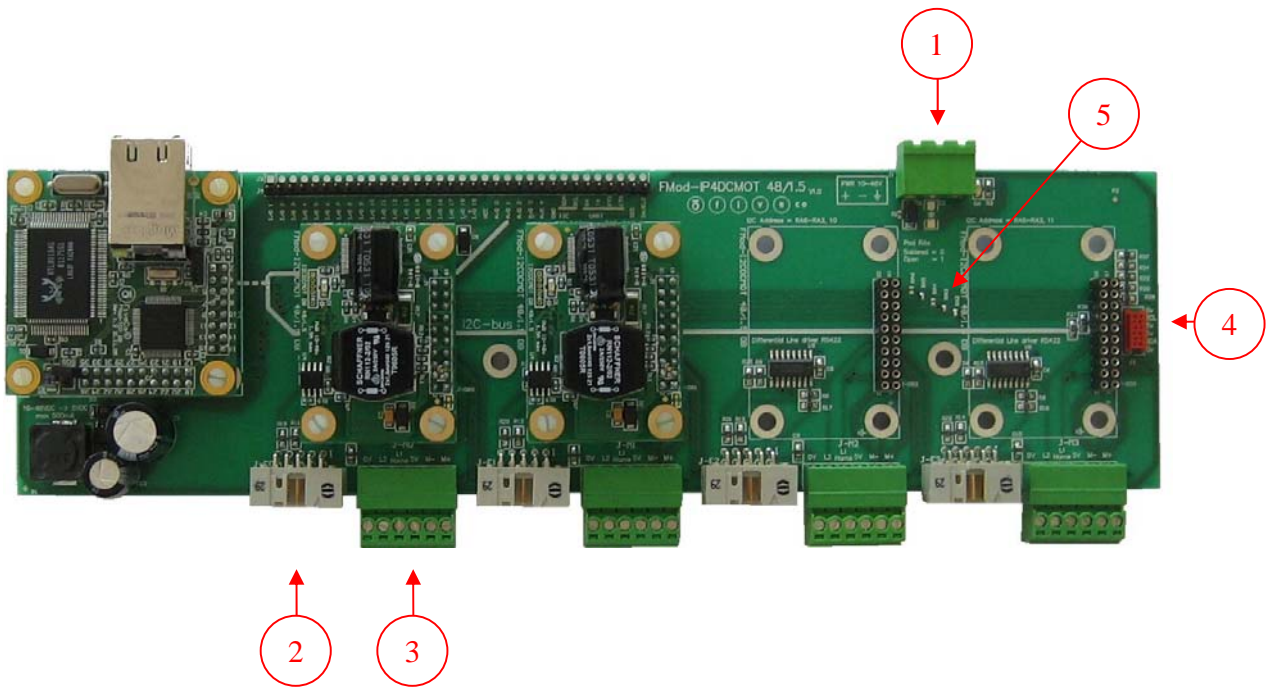


FMod-IP4DCMOT 48/I.5



I - Power supply

1	Shield
2	GND
3	10 – 48 VDC In

2 - Encoder

1	n.c.
2	5 VDC Out
3	GND
4	n.c.
5	\overline{A}
6	A
7	\overline{B}
8	B
9	\overline{T}
10	T

3 - Motor

1	GND
2	Limit 2
3	Limit 1 (home)
4	5 VDC Out
5	Motor -
6	Motor +

4 - I2C bus

1	GND
2	SDA
3	5 VDC Out
4	5 VDC Out
5	SCL
6	GND

5 - I2C Base address selection [bits 6-2]

Open : corresponding address bit is 1

Closed : corresponding address bit is 0